# **T545 Series High Energy Polymer Tantalum**



#### **Overview**

The KEMET Organic Capacitor is a tantalum capacitor with a Ta anode and  ${\rm Ta_2O_5}$  dielectric. A conductive organic polymer replaces the traditionally used  ${\rm MnO_2}$  as the cathode plate of the capacitor. This results in very low ESR and improved capacitance retention at high frequency. The polymer technology also exhibits a benign failure mode which eliminates the ignition failures that can occur in standard  ${\rm MnO_2}$  tantalum types. Ta polymers may be operated at voltages up to 90% of rated voltage for part types with rated voltages of  $\leq$  10 volts and up to 80% of rated voltage for part types > 10 volts with equivalent or better reliability than traditional  ${\rm MnO_2}$  tantalum capacitors operated at 50% of rated voltage.

The T545 Series was developed to deliver the highest energy per CC of any tantalum surface mount device (SMD). This capability makes this capacitor an excellent solution for designs requiring high energy at relatively low voltages, such as data hardening or data vaulting for solid state drives (SSD's). The T545 Series High Energy Polymer Tantalum Surface Mount Capacitor captures the best features of multilayer ceramic capacitors (low ESR and high frequency capacitance retention), aluminum electrolytics (higher capacitance and benign failure mode) and proven solid tantalum technology (volumetric efficiency, surface mount capability and extremely long life). In addition, this series is subjected to 100% thermal shock and voltage aging to insure long term reliability.

#### **Benefits**

- · Extremely low ESR
- · High energy delivery capability
- -55°C to 85°C operating temperature range
- Polymer cathode technology
- · High frequency capacitance retention
- · Non-ignition failure mode
- Capacitance up to 1,500 μF
- Voltage: 6.3 16 V
- 100% accelerated steady state aging
- 100% surge current tested
- · 100% thermal shock
- Volumetric efficiency, very high capacitance
- · Self-healing mechanism
- Taped and reeled per EIA 481-1, EIA standard case sizes

### **Applications**

Typical applications include hold-up, data hardening or vaulting for enterprise and military SSDs, and high end desktop modems.



# **Environmental Compliance**

RoHS Compliant (6/6) according to Directive 2002/95/EC when ordered with 100% Sn solder.



RoHS Compliant



#### **SPICE**

For a detailed analysis of specific part numbers, please visit www.kemet.com for a free download of KEMET's SPICE software. The KEMET SPICE program is freeware intended to aid design engineers in analyzing the performance of these capacitors over frequency, temperature, ripple, and DC bias conditions.

# **Ordering Information**

T	545	Н	108	M	006	Α	T	E055
Capacitor Class	Series	Case Size	Capacitance Code (pF)	Capacitance Tolerance	Voltage	Failure Rate/ Design	Lead Material	ESR
T = Tantalum	High Energy Polymer Tantalum	H, W, Y, X, V, D	First two digits represent significant figures. Third digit specifies number of zeros.	M = ±20%	006 = 6.3 V 010 = 10 V 016 = 16 V	A = N/A	T = 100% Tin (Sn)	ESR in miliOhms

### **Performance Characteristics**

Item	Performance Characteristics		
Operating Temperature	-55°C to 85°C		
Rated Capacitance Range	47 μF – 1,500 μF @ 120 Hz/25°C		
Capacitance Tolerance	M Tolerance (20%)		
Rated Voltage Range	6.3 – 16 V		
DF (120 Hz)	Refer to Part Number Electrical Specification Table		
ESR (100 KHz)	Refer to Part Number Electrical Specification Table		
Leakage Current	≤ 0.1 CV (µA) at rated voltage after 5 minutes		



# Qualification

Test	Condition			Characteristics			
		Δ C/C	Δ C/C Within -20/+10 of initial value				
Freduces	05°C @ retad welters 2 000 become**	DF	Within initial limits				
Endurance	85°C @ rated voltage, 2,000 hours**		DCL	Within 1.25 x ini	tial limit		
				Within 2.0 x initi	al limit		
			Δ C/C	Within -20/+10 c	of initial value		
Storage Life	85°C @ 0 volts, 2,000 hours**		DF	Within initial limi	its		
	85 C @ 0 Volts, 2,000 flours		DCL	Within 1.25 x ini	tial limit		
		ESR	Within 2.0 x initi	al limit			
			Δ C/C	Within -5%/+35% of initial value			
Humidity	60°C, 90% RH, 500 hours	DF	Within initial limits				
Trumuity	00 C, 90 % KH, 500 Hours	DCL	Within 5.0 x initi	al limit			
		ESR	Within 2.0 x initi	Within 2.0 x initial limit			
			+25°C	-55°C	+85°C		
Temperature Stability	Extreme temperature exposure at a succession of continuous steps at +25°C,	Δ C/C	IL*	+/-20%	+/-20%		
Temperature Stability	-55°C, +25°C, +85°C, +105°C, +25° C	DF	IL	IL	1.2 x IL		
		DCL	IL	n/a	10 x IL		
			Δ C/C	Within -20/+10 c	of initial value		
Surge Voltage	85°C, 1.32 x rated voltage, 1,000 cycles		DF	Within initial limits			
Surge voltage	03 G, 1.32 X rated voltage, 1,000 cycles		DCL	Within initial limits			
			ESR	Within initial limits			
	MIL-STD-202, Method 213, Condition I, 100 G	MII – STD–202 Method 213 Condition I 100 G peak			itial value		
Mechanical Shock/Vibration	MIL-STD-202, Method 204, Condition D, 10 Hz		DF	Within initial limi	Within initial limits		
	20 G peak		DCL	Within initial limi	its		

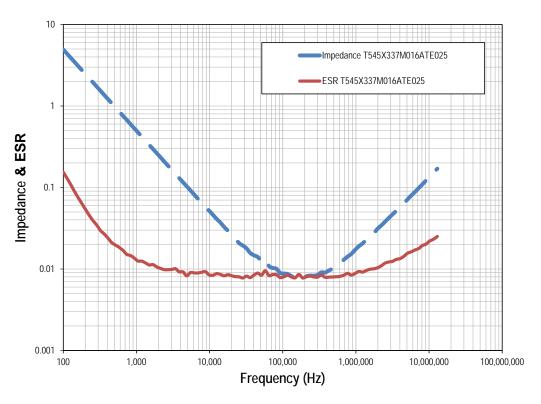
<sup>\*</sup>IL = Initial limit

<sup>\*\*</sup>minimum temperature test condition 85°C

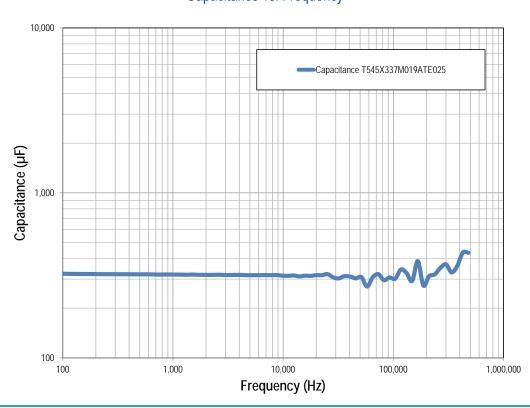


### **Electrical Characteristics**

# ESR vs. Frequency

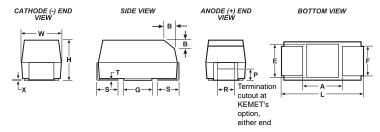


### Capacitance vs. Frequency





### **Dimensions – Millimeters**



Case	Size	Component												
KEMET	EIA	L*	W*	H*	F* ±0.1 ±(0.004)	S* ±0.3 ±(0.012)	B* ±0.15 (Ref) ±0.006	X (Ref)	P (Ref)	R (Ref)	T (Ref)	A (Min)	G (Ref)	E (Ref)
D	7343–31	7.3 ±0.3 (0.287 ±0.012)	4.3 ±0.3 (0.169 ±0.012)	2.8 ±0.3 (0.110 ±0.012)	2.4 (0.094)	1.3 (0.051)	0.5 (.020)	0.10 ± 0.10 (.004 ± .004)	0.9 (.035)	1.0 (.039)	0.13 (.005)	3.8 (.150)	3.5 (.138)	3.5 (.138)
Н	7260–20	7.3 ±0.3 (0.287 ±0.012)	6.0±0.3 (0.236 ±0.012)	2.0 (0.078) Maximum	4.1 (0.161)	1.3 (0.051)	n/a	0.10 ± 0.10 (.004 ± .004)	0.9 (.035)	1.0 (.039)	0.13 (.005)	3.3 (0.13)	3.5 (.138)	3.5 (.138)
V	7343–20	7.3 ±0.3 (0.287 ±0.012)	4.3 ±0.3 (0.169 ±0.012)	2.0 (0.079)	2.4 (0.094)	1.3 (0.051)	n/a	0.05 (.002)	n/a	n/a	0.13 (.005)	3.8 (.150)	3.5 (.138)	3.5 (.138)
W	7343–15	7.3 ±0.3 (0.287 ±0.012)	4.3 ±0.3 (0.169 ±0.012)	1.5 (0.059)	2.4 (0.094)	1.3 (0.051)	n/a	0.05 (.002)	n/a	n/a	0.13 (.005)	3.8 (.150)	3.5 (.138)	3.5 (.138)
Х	7343–43	7.3 ±0.3 (0.287 ±0.012)	4.3 ±0.3 (0.169 ±0.012)	4.0 ±0.3 (0.157 ±0.012)	2.4 (0.094)	1.3 (0.051)	0.5 (.020)	0.10 ± 0.10 (.004 ± .004)	1.7 (.067)	1.0 (.039)	0.13 (.005)	3.8 (.150)	3.5 (.138)	3.5 (.138)
Υ	7343–40	7.3 ± 0.3 (.287 ± .012)	4.3 ± 0.3 (.169 ± .012)	4.0 (.157)	2.4 (.094)	1.3 (.051)	0.5 (.020)	0.10 ± 0.10 (.004 ± .004)	1.7 (.067)	1.0 (.039)	0.13 (.005)	3.8 (.150)	3.5 (.138)	3.5 (.138)

Notes: (Ref) – Dimensions provided for reference only. No dimensions are provided for B, P or R because low profile cases do not have a bevel or a notch.

# Table 1 – Ratings & Part Number Reference

Rated Voltage	Rated Capacitance	Case Code/ Case Size	KEMET Part Number	DC Leakage	DF	ESR	Maximum Allowable Ripple Current	MSL	Rated Temperature	Energy (mJ)
V	μF	KEMET/EIA		(μΑ) @ V <sub>R</sub> , 20°C Maximum/ 5 Minutes	% @ 20°C 120 Hz Maximum	(mW) @ 20°C 100 kHz Maximum	(mAmps) 45°C 100 kHz	Reflow Temperature ≤ 260°C	(°C)	(½CVa²) - (½CVd²) Va = Voltage Applied Vd = Voltage Drop
6.3	1000	H/7260-20	T545H108M006ATE055	630.0	20	55	1850.0	4	85	11.57
6.3	1500	H/7260-20	T545H158M006ATE035	945.0	20	35	2300.0	4	85	17.36
6.3	1500	H/7260-20	T545H158M006ATE055	945.0	20	55	1850.0	4	85	17.36
6.3	470	W/7343-15	T545W477M006ATE035	296.0	10	35	2300.0	4	85	5.44
6.3	470	W/7343-15	T545W477M006ATE055	296.0	10	55	1800.0	4	85	5.44
6.3	470	Y/7343-40	T545Y477M006ATE025	296.0	10	25	3100.0	3	85	5.44
6.3	470	X/7343-43	T545X477M006ATE006	296.0	10	6	6700.0	3	85	5.44
10	100	W/7343-15	T545W107M010ATE040	100.0	10	40	2100.0	4	85	3.60
10	330	Y/7343-40	T545Y337M010ATE035	330.0	10	35	2600.0	3	85	11.88
10	330	X/7343-43	T545X337M010ATE006	330.0	10	6	6700.0	3	85	11.88
16	47	W/7343-15	T545W476M016ATE045	75.0	10	45	2000.0	3	85	3,64
16	220	X/7343-43	T545X227M016ATE035	352.0	10	35	2700.0	3	85	17.03
16	330	X/7343-43	T545X337M016ATE025	528.0	10	25	3300.0	3	85	25.55
16	100	V/7343-19	T545V107M016ATE055	160.0	10	55	1850.0	3	85	7.74
16	100	D/7343-31	T545D107M016ATE055	160.0	10	55	2050.0	3	85	7.74
V	μF	KEMET/EIA		(μΑ) @ V <sub>R</sub> , 20°C Maximum/ 5 Minutes	% @ 20°C 120 Hz Maximum	(mW) @ 20°C 100 kHz Maximum	(mAmps) 45°C 100 kHz	Reflow Temperature ≤ 260°C	(°C)	(½CVa²) - (½CVd²) Va = Voltage Applied Vd = Voltage Drop
Rated Voltage	Rated Capacitance	Case Code/ Case Size	KEMET Part Number	DC Leakage	DF	ESR	Maximum Allowable Ripple Current	MSL	Rated Temperature	Energy (mJ)

<sup>\*</sup> MIL-C-55365/8 specified dimensions



## **Derating Guidelines**

Voltage Rating	Maximum Recommended Steady State Voltage	Maximum Recommended Transient Voltage (1 ms – 1 μs)						
	-55°C to 85°C							
6.3 V ≤ V <sub>R</sub> ≤ 10 V	90% of $V_{\scriptscriptstyle R}$	$V_{R}$						
10 V < V <sub>R</sub> ≤ 16 V	80% of V <sub>R</sub>	$V_R$						

 $V_R$ = Rated Voltage

### Ripple Current/Ripple Voltage

Permissible AC ripple voltage and current are related to equivalent series resistance (ESR) and the power dissipation capabilities of the device. Permissible AC ripple voltage which may be applied is limited by two criteria:

- 1. The positive peak AC voltage plus the DC bias voltage, if any, must not exceed the DC voltage rating of the capacitor.
- The negative peak AC voltage in combination with bias voltage, if any, must not exceed the allowable limits specified for reverse voltage. See the Reverse Voltage section for allowable limits.

The maximum power dissipation by case size can be determined using the table at right. The maximum power dissipation rating stated in the table must be reduced with increasing environmental operating temperatures. Refer to the table below for temperature compensation requirements.

	ure Compensation Naximum Power Dissi							
≤ 45°C	45° C < T ≤ 85°C	85°C < T ≤ 125°C						
1.00 0.70 0.25								

T= Environmental Temperature

Using the P max of the device, the maximum allowable rms ripple current or voltage may be determined.

 $I(max) = \sqrt{P \max/R}$  $E(max) = \sqrt{P \max^*R}$ 

*I = rms ripple current (amperes)* 

*E* = rms ripple voltage (volts)

P max = maximum power dissipation (watts)

R = ESR at specified frequency (ohms)

Case Co	Maximum Power Dissipation (P max) mWatts @ 45°C with +30°C Rise		
KEMET	EIA		
T520T/T525T/T540T/ T543T	3528–12	105	
T520M/T543M	3528-15	120	
T520A/T543A	3216-18	112	
T520B/T525B/T540B/ T543B	3538–21	127	
T520U/T543U	6032-15	135	
T520L/T543L	3528-19	150	
T520C/T543C	6032–28	165	
T520W/T545W/T543W	7343–15	180	
T520V/T521V/T522V/ T545V/T543V	7343–20	187	
T520D/T521D/T525D/ T540D/T545D/T543D	7343–31	225	
T520Y/T522Y/T525Y/ T543Y	7343–40	241	
T520X/T521X/T545X/ T543X	7343–43	247	
T545E	7260-38	345	
T520H/T545H/T543H	7260–20	187	
T528I	3216–10	95	
T528K	3528-10	150	
T528W	7343–15	325	
T528Z	7343–17	325	
T530/T541D	7343–31	255	
T530/T541Y	7343–40	263	
T530/T541X	7443–43	270	

The maximum power dissipation rating must be reduced with increasing environmental operating temperatures. Refer to the Temperature Compensation Multiplier table for details.



### **Reverse Voltage**

Polymer tantalum capacitors are polar devices and may be permanently damaged or destroyed if connected in the wrong polarity. These devices will withstand a small degree of transient voltage reversal for short periods as shown in the below table.

Temperature	Permissible Transient Reverse Voltage
25°C	15% of Rated Voltage
55°C	10% of Rated Voltage
85°C	5% of Rated Voltage
105°C	3% of Rated Voltage
125°C*	1% of Rated Voltage

<sup>\*</sup>For series rated to 125°C

**Table 2 – Land Dimensions/Courtyard** 

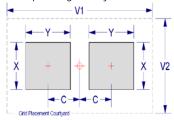
KEMET	Metric Size Code	Density Level A: Maximum (Most) Land Protrusion (mm)			Density Level B: Median (Nominal) Land Protrusion (mm)				Density Level C: Minimum (Least) Land Protrusion (mm)							
Case	EIA	Χ	Υ	С	V1	V2	Х	Υ	С	V1	V2	Χ	Υ	С	V1	V2
Α	3216–18	1.35	2.15	1.45	6.10	2.80	1.25	1.75	1.35	5.00	2.30	1.15	1.35	1.25	4.10	2.00
В	3258–19	2.35	2.15	1.45	6.10	4.00	2.25	1.75	1.35	5.00	3.50	2.15	1.35	1.25	4.10	3.20
С	6032–25	2.35	2.65	2.60	8.90	4.40	2.25	2.25	2.50	7.80	3.90	2.15	1.85	2.40	6.90	3.60
D	7343–31	2.55	3.75	2.70	10.20	5.50	2.45	3.35	2.60	9.10	5.00	2.35	2.95	2.50	8.20	4.70
Н	7260-20	4.25	2.65	3.20	10.10	7.20	4.15	2.25	3.30	9.40	6.70	4.05	1.85	3.00	8.10	6.40
E¹	7260–38	4.25	2.65	3.20	10.10	7.20	4.15	2.25	3.30	9.40	6.70	4.05	1.85	3.00	8.10	6.40
R	2012–12	1.05	1.80	1.00	4.80	2.40	0.95	1.45	0.90	3.80	1.90	0.85	1.05	0.80	2.90	1.60
S²	3216–12	1.35	2.15	1.45	6.10	2.80	1.25	1.75	1.35	5.00	2.30	1.15	1.35	1.25	4.10	2.00
T	3258–12	2.35	2.15	1.45	6.10	4.00	2.25	1.75	1.35	5.00	3.50	2.15	1.35	1.25	4.10	3.20
U	6032–15	2.35	2.65	2.60	8.90	4.40	2.25	2.25	2.50	7.80	3.90	2.15	1.85	2.40	6.90	3.60
V	7343–20	2.55	3.75	2.70	10.20	5.50	2.45	3.35	2.60	9.10	5.00	2.35	2.95	2.50	8.20	4.70
X¹	7343–43	2.55	3.75	2.70	10.20	5.50	2.45	3.35	2.60	9.10	5.00	2.35	2.95	2.50	8.20	4.70
Y <sup>1</sup>	7343–35	2.55	3.75	2.70	10.20	5.50	2.45	3.35	2.60	9.10	5.00	2.35	2.95	2.50	8.20	4.70

Density Level A: For low-density product applications. Recommended for wave solder applications and provides a wider process window for reflow solder processes.

Density Level B: For products with a moderate level of component density. Provides a robust solder attachment condition for reflow solder processes.

Density Level C: For high component density product applications. Before adapting the minimum land pattern variations the user should perform qualification testing based on the conditions outlined in IPC Standard 7351 (IPC-7351).

<sup>&</sup>lt;sup>2</sup> Land pattern geometry is too small for silkscreen outline.



<sup>&</sup>lt;sup>1</sup> Height of these chips may create problems in wave soldering.



### **Soldering Process**

KEMET's families of surface mount capacitors are compatible with wave (single or dual), convection, IR, or vapor phase reflow techniques. Preheating of these components is recommended to avoid extreme thermal stress. KEMET's recommended profile conditions for convection and IR reflow reflect the profile conditions of the IPC/J-STD-020D standard for moisture sensitivity testing. The devices can safely withstand a maximum of three reflow passes at these conditions.

Please note that although the X/7343–43 case size can withstand wave soldering, the tall profile (4.3 mm maximum) dictates care in wave process development.

Hand soldering should be performed with care due to the difficulty in process control. If performed, care should be taken to avoid contact of the soldering iron to the molded case. The iron should be used to heat the solder pad, applying solder between the pad and the termination, until reflow occurs. Once reflow occurs, the iron should be removed immediately. "Wiping" the edges of a chip and heating the top surface is not recommended.

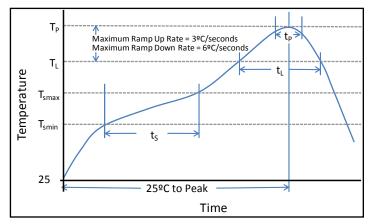
During typical reflow operations, a slight darkening of the goldcolored epoxy may be observed. This slight darkening is normal and not harmful to the product. Marking permanency is not affected by this change.

Profile Feature	SnPb Assembly	Pb-Free Assembly
Preheat/Soak		
Temperature Minimum (T <sub>Smin</sub> )	100°C	150°C
Temperature Maximum (T <sub>Smax</sub> )	150°C	200°C
Time $(t_s)$ from $T_{smin}$ to $T_{smax}$ )	60 – 120 seconds	60 – 120 seconds
Ramp-up Rate (T <sub>L</sub> to T <sub>P</sub> )	3°C/seconds maximum	3°C/seconds maximum
Liquidous Temperature (T <sub>L</sub> )	183°C	217°C
Time Above Liquidous (t <sub>L</sub> )	60 – 150 seconds	60 – 150 seconds
Peak Temperature (T <sub>P</sub> )	220°C* 235°C**	250°C* 260°C**
Time within 5°C of Maximum Peak Temperature (t <sub>P</sub> )	20 seconds maximum	30 seconds maximum
Ramp-down Rate (T <sub>P</sub> to T <sub>L</sub> )	6°C/seconds maximum	6°C/seconds maximum
Time 25°C to Peak Temperature	6 minutes maximum	8 minutes maximum

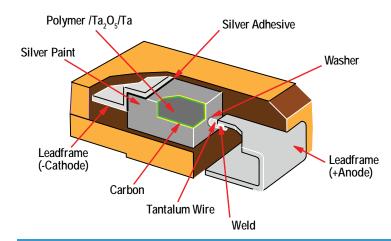
Note: All temperatures refer to the center of the package, measured on the package body surface that is facing up during assembly reflow.

\*Case Size D, E, P, Y, and X

\*\*Case Size A. B. C. H. I. K. M. R. S. T. U. V. W. and Z

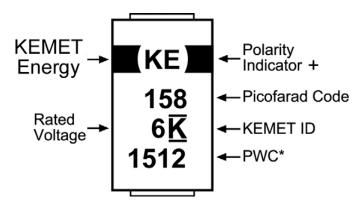


#### Construction





### **Capacitor Marking**



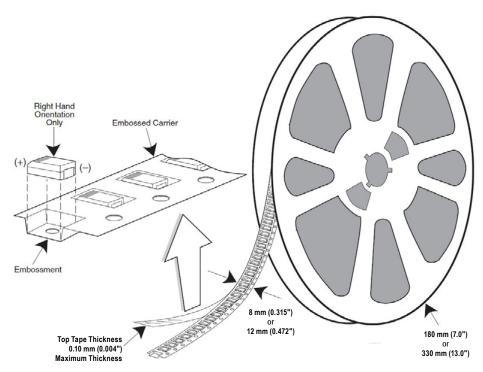
# **Storage**

All KO-CAP Series are shipped in moisture barrier bags with a desiccant and moisture indicator card. These series are classified as MSL3 (Moisture Sensitivity Level 3). Product contained within the moisture barrier bags should be stored in normal working environments with temperatures not to exceed 40°C and humidity not in excess of 60% RH.



## **Tape & Reel Packaging Information**

KEMET's molded tantalum and aluminum chip capacitor families are packaged in 8 and 12 mm plastic tape on 7" and 13" reels in accordance with *EIA Standard 481–1*: Embossed Carrier Taping of Surface Mount Components for Automatic Handling. This packaging system is compatible with all tape-fed automatic pick-and-place systems.



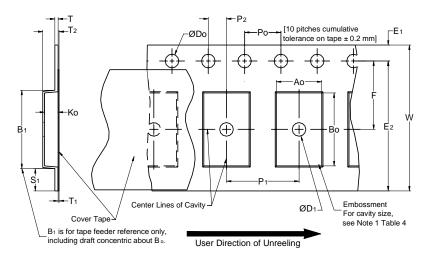
**Table 3 – Packaging Quantity** 

Case	Code	Tape Width (mm)	7" Reel*	13" Reel*
KEMET	EIA			
R	2012-12	8	2,500	10,000
I	3216-10	8	3,000	12,000
S	3216-12	8	2,500	10,000
Т	3528-12	8	2,500	10,000
М	3528-15	8	2,000	8,000
U	6032-15	12	1,000	5,000
L	6032-19	12	1,000	5,000
W	7343-15	12	1,000	3,000
Z	7343-17	12	1,000	3,000
V	7343-20	12	1,000	3,000
Α	3216-18	8	2,000	9,000
В	3528-21	8	2,000	8,000
С	6032-28	12	500	3,000
D	7343-31	12	500	2,500
Υ	7343-40	12	500	2,000
Х	7343-43	12	500	2,000
E/T428P	7260-38	12	500	2,000
Н	7260-20	12	500	2,000

<sup>\*</sup> No C-Spec required for 7" reel packaging. C-7280 required for 13" reel packaging.



## Figure 1 – Embossed (Plastic) Carrier Tape Dimensions



## **Table 4 – Embossed (Plastic) Carrier Tape Dimensions**

Metric will govern

Constant Dimensions — Millimeters (Inches)									
Tape Size	D <sub>0</sub>	D <sub>1</sub> Minimum Note 1	E <sub>1</sub>	P <sub>0</sub>	P <sub>2</sub>	R Reference Note 2	S <sub>1</sub> Minimum Note 3	T Maximum	T <sub>1</sub> Maximum
8 mm		1.0 (0.039)				25.0 (0.984)			
12 mm	1.5 +0.10/-0.0 (0.059 +0.004/-0.0)	1.5	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	30	0.600 (0.024)	0.600 (0.024)	0.100 (0.004)
16 mm		(0.059)				(1.181)			
Variable Dimensions — Millimeters (Inches)									
Tape Size	Pitch	B <sub>1</sub> Maximum Note 4	E <sub>2</sub> Minimum	F	P <sub>1</sub>	T <sub>2</sub> Maximum	W Maximum	A <sub>0</sub> ,B <sub>0</sub>	. & K <sub>0</sub>
8 mm	Single (4 mm)	4.35 (0.171)	6.25 (0.246)	3.5 ±0.05 (0.138 ±0.002)	4.0 ±0.10 (0.157 ±0.004)	2.5 (0.098)	8.3 (0.327)		
12 mm	Single (4 mm) & Double (8 mm)	8.2 (0.323)	10.25 (0.404)	5.5 ±0.05 (0.217 ±0.002)	8.0 ±0.10 (0.315 ±0.004)	4.6 (0.181)	12.3 (0.484)	Note 5	
16 mm	Triple (12 mm)	12.1 (0.476)	14.25 (0.561)	5.5 ±0.05 (0.217 ±0.002)	8.0 ±0.10 (0.315 ±0.004)	4.6 (0.181)	16.3 (0.642)		

- 1. The embossment hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location and hole location shall be applied independent of each other.
- 2. The tape, with or without components, shall pass around R without damage (see Figure 5).
- 3. If S, < 1.0 mm, there may not be enough area for cover tape to be properly applied (see EIA Standard 481–D, paragraph 4.3, section b).
- 4.  $B_1$  dimension is a reference dimension for tape feeder clearance only.
- 5. The cavity defined by  $A_0$ ,  $B_0$  and  $K_0$  shall surround the component with sufficient clearance that:
  - (a) the component does not protrude above the top surface of the carrier tape.
  - (b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.
  - (c) rotation of the component is limited to 20° maximum for 8 and 12 mm tapes and 10° maximum for 16 mm tapes (see Figure 2).
  - (d) lateral movement of the component is restricted to 0.5 mm maximum for 8 mm and 12 mm wide tape and to 1.0 mm maximum for 16 mm tape (see Figure 3).
  - (e) see Addendum in EIA Standard 481-D for standards relating to more precise taping requirements.



## **Packaging Information Performance Notes**

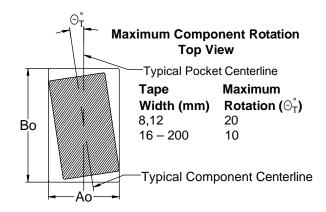
- 1. Cover Tape Break Force: 1.0 Kg minimum.
- 2. Cover Tape Peel Strength: The total peel strength of the cover tape from the carrier tape shall be:

Tape Width	Peel Strength		
8 mm	0.1 to 1.0 Newton (10 to 100 gf)		
12 and 16 mm	0.1 to 1.3 Newton (10 to 130 gf)		

The direction of the pull shall be opposite the direction of the carrier tape travel. The pull angle of the carrier tape shall be 165 $^{\circ}$  to 180 $^{\circ}$  from the plane of the carrier tape. During peeling, the carrier and/or cover tape shall be pulled at a velocity of 300  $\pm$ 10 mm/minute.

**3. Labeling:** Bar code labeling (standard or custom) shall be on the side of the reel opposite the sprocket holes. *Refer to EIA Standards 556 and 624.* 

# Figure 2 – Maximum Component Rotation



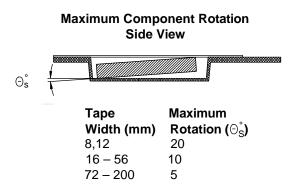


Figure 3 – Maximum Lateral Movement

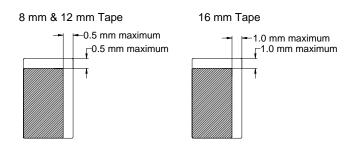


Figure 4 - Bending Radius

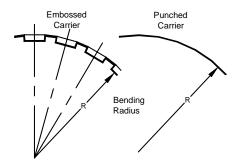
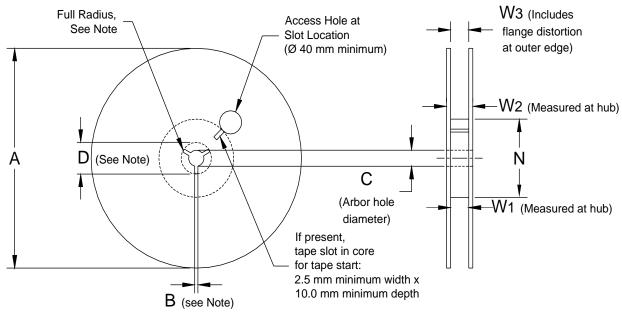




Figure 5 – Reel Dimensions



Note: Drive spokes optional; if used, dimensions B and D shall apply.

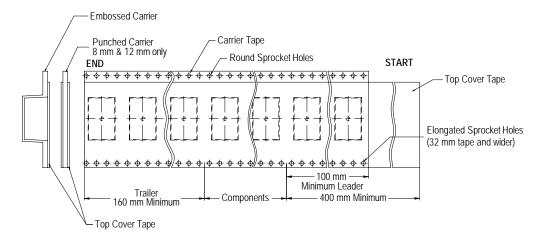
**Table 5 - Reel Dimensions** 

Metric will govern

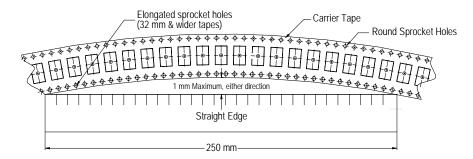
Constant Dimensions — Millimeters (Inches)					
Tape Size	A	B Minimum	С	D Minimum	
8 mm	178 ±0.20 (7.008 ±0.008)				
12 mm	or	1.5 (0.059)	13.0 +0.5/-0.2 (0.521 +0.02/-0.008)	20.2 (0.795)	
16 mm	330 ±0.20 (13.000 ±0.008)	(0.000)	(0.021 10.027 0.000)	(000)	
Variable Dimensions — Millimeters (Inches)					
Tape Size	N Minimum	W <sub>1</sub>	W <sub>2</sub> Maximum	W <sub>3</sub>	
8 mm		8.4 +1.5/-0.0 (0.331 +0.059/-0.0)	14.4 (0.567)		
12 mm	50 (1.969)	12.4 +2.0/-0.0 (0.488 +0.078/-0.0)	18.4 (0.724)	Shall accommodate tape width without interference	
16 mm	. ,	16.4 +2.0/-0.0 (0.646 +0.078/-0.0)	22.4 (0.882)		



# Figure 6 – Tape Leader & Trailer Dimensions



# Figure 7 – Maximum Camber





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